FIG.1

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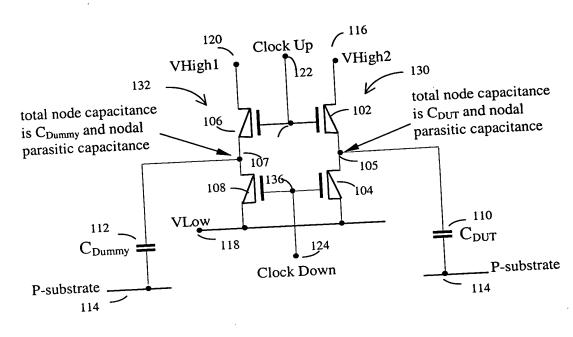


FIG. 2

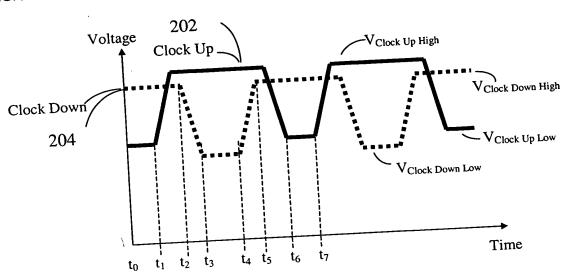


FIG.3

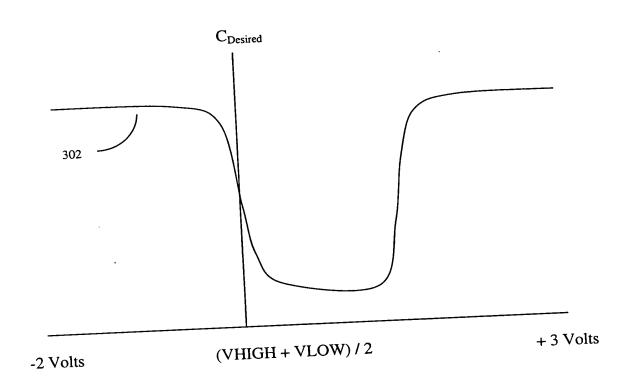
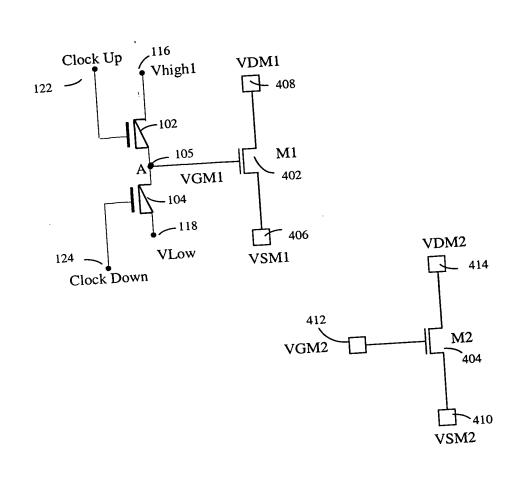
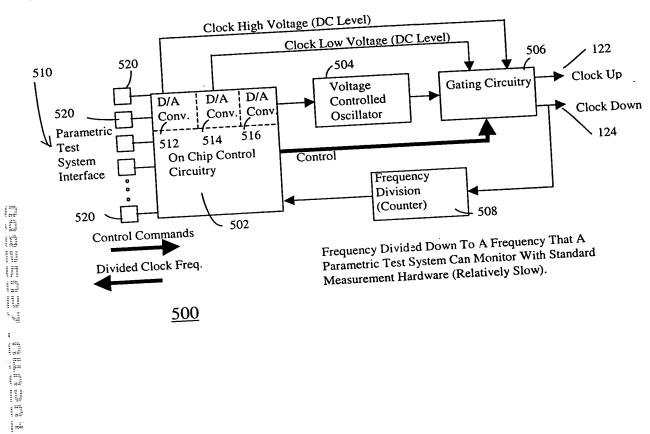


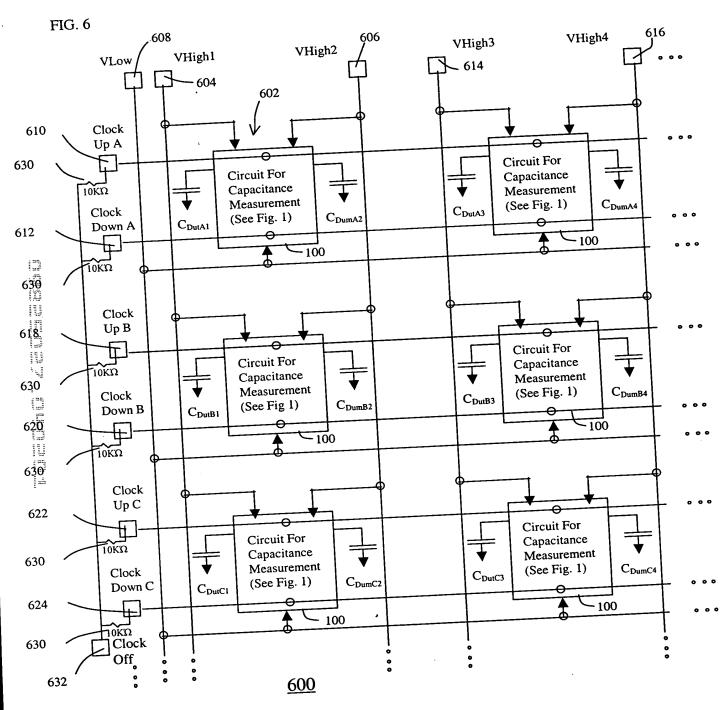
FIG. 4

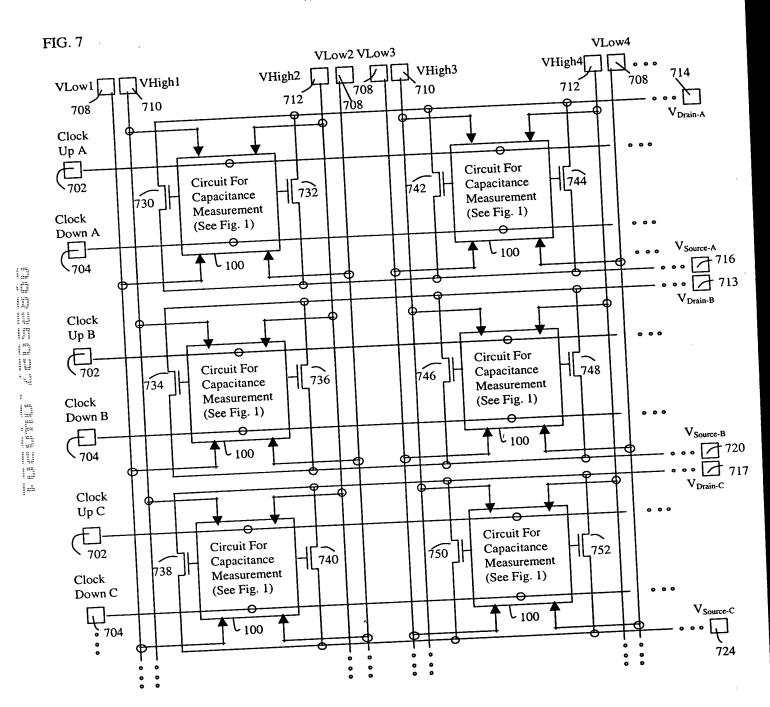


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FIG. 5







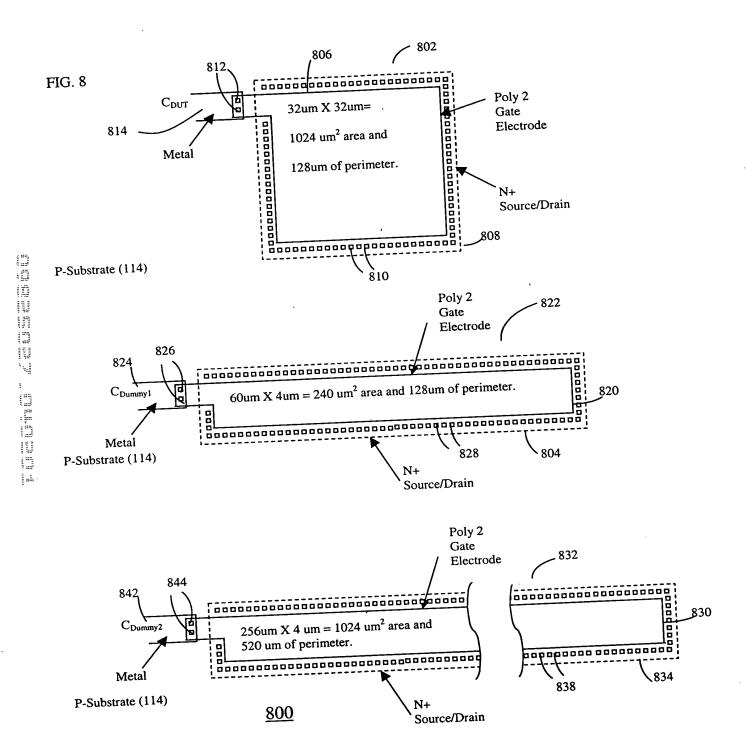


FIG. 9

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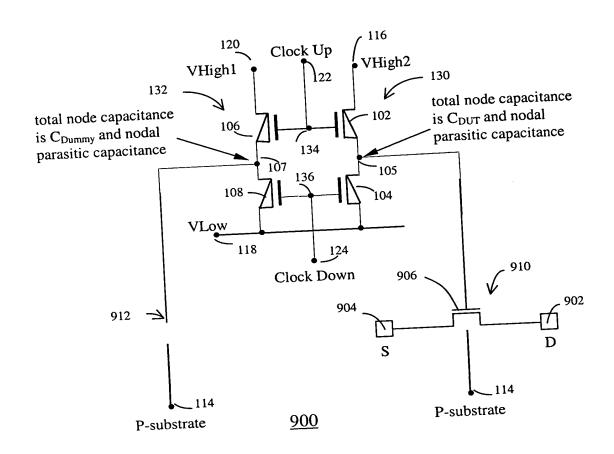
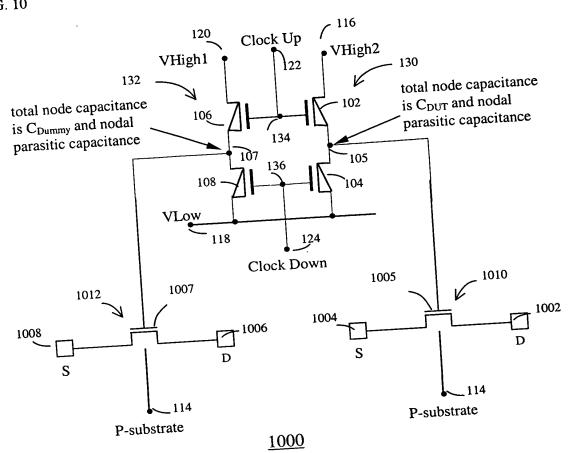


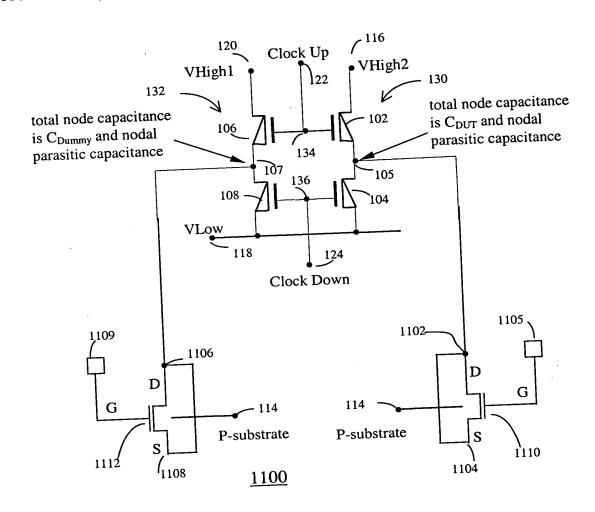
FIG. 10



YOLTAGE-VARIABLE

FIG. 11

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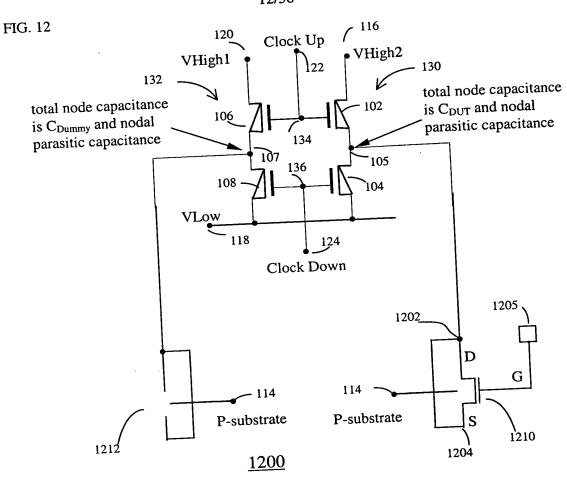


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D BASED METHOD AND DEVICES FOR MEASUR CITANCES AND OTHER ON-CHIP PARAMETER Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

VOLTAGE-VARIABLE

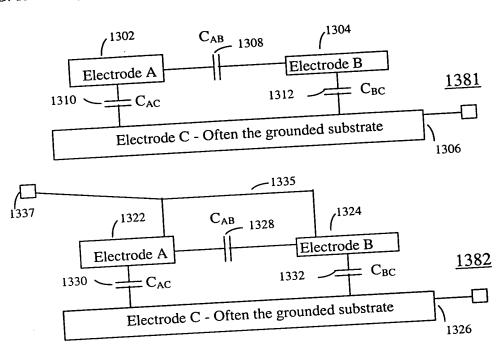


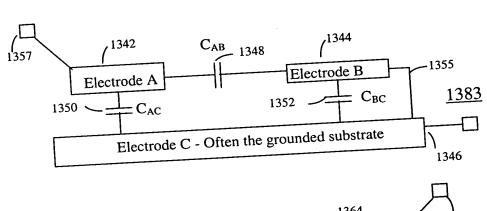


VOLTAGE-VARIABLE

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FIG. 13





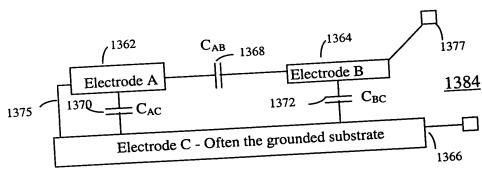


FIG. 14

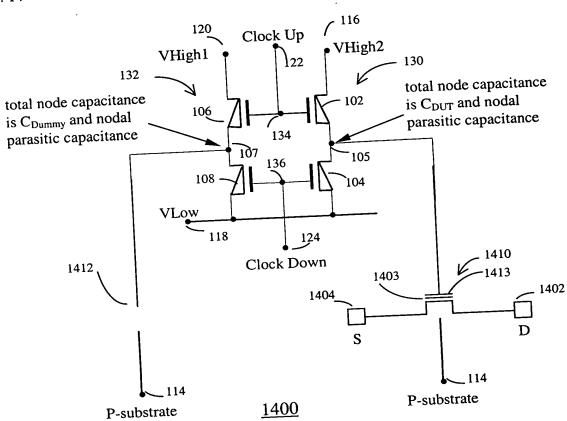


FIG. 15

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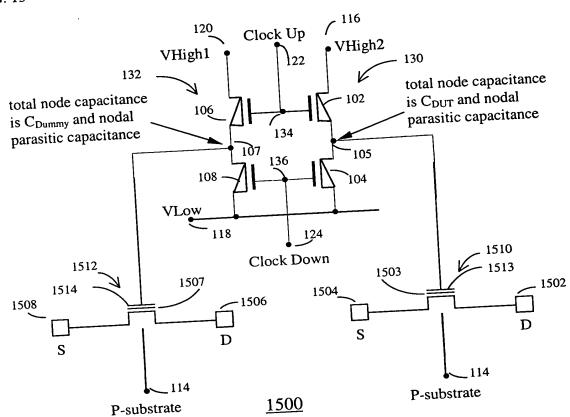


FIG. 16

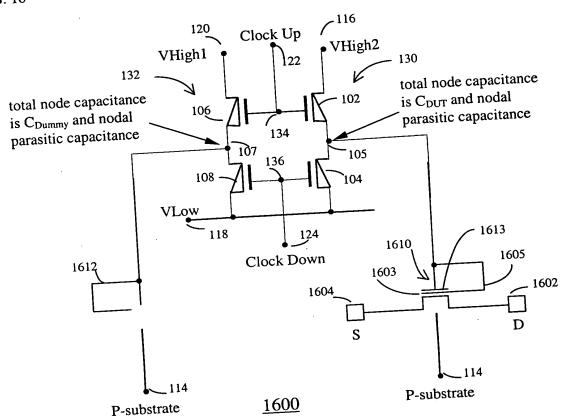


FIG. 17

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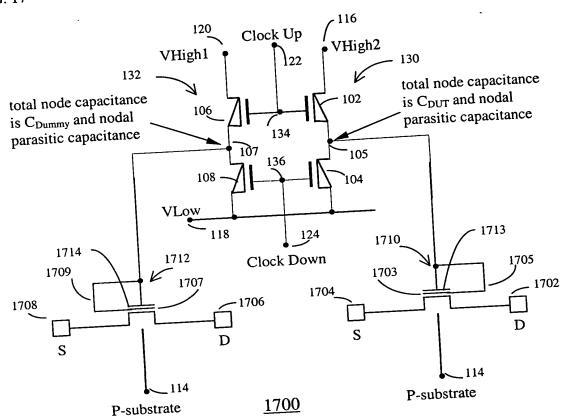


FIG. 18

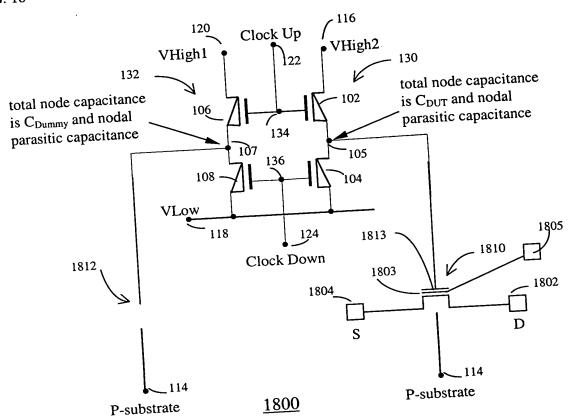


FIG. 19

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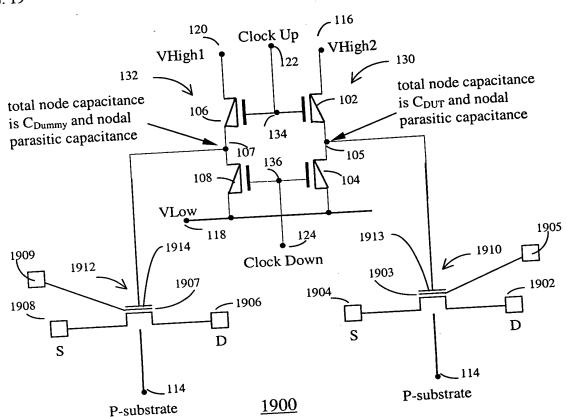


FIG. 20

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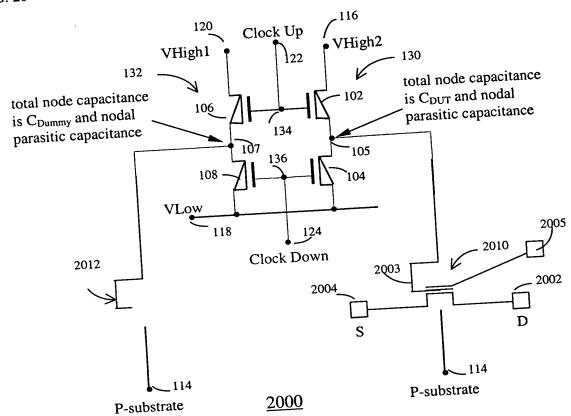


FIG. 21

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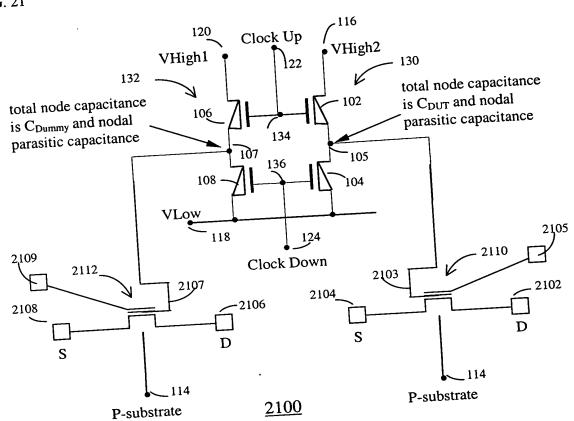
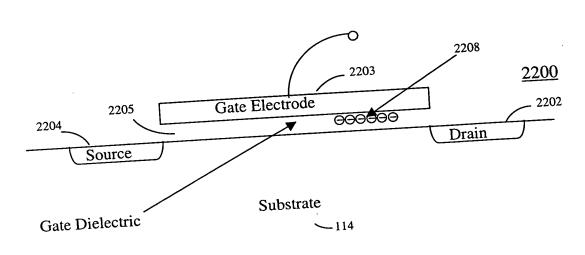
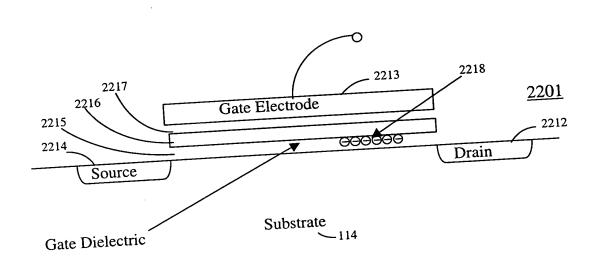


FIG. 22

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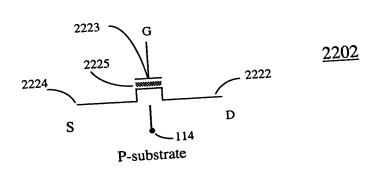


FIG. 23:

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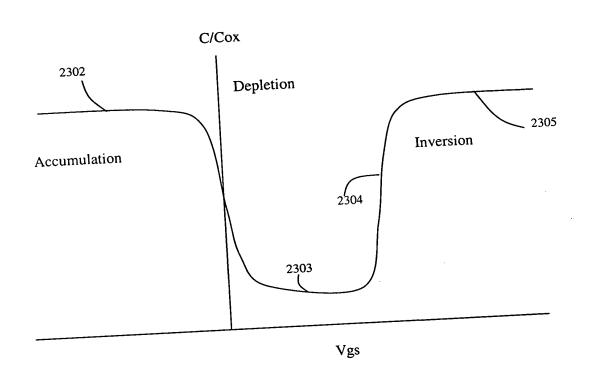


FIG. 24

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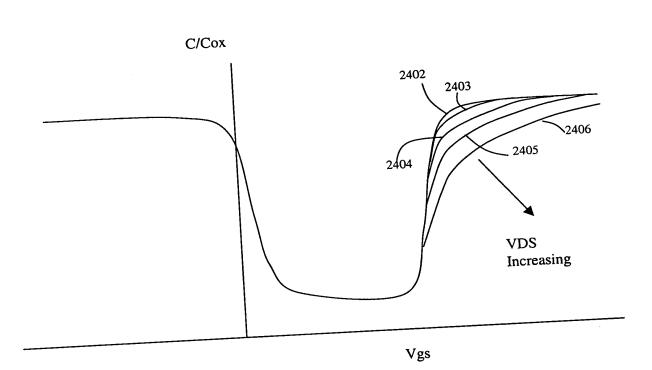


FIG. 25

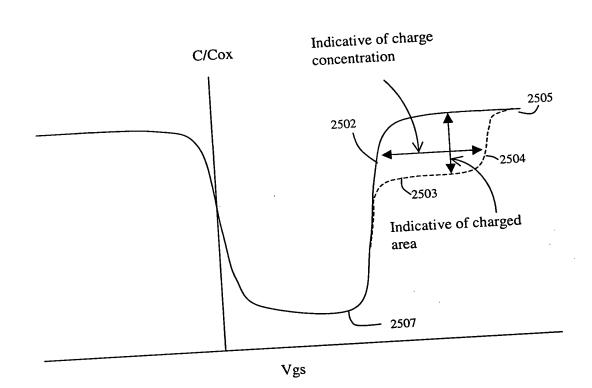


FIG. 26

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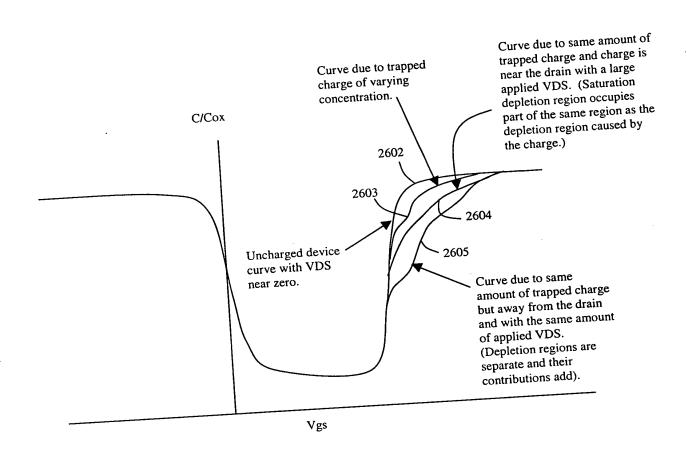


FIG.27

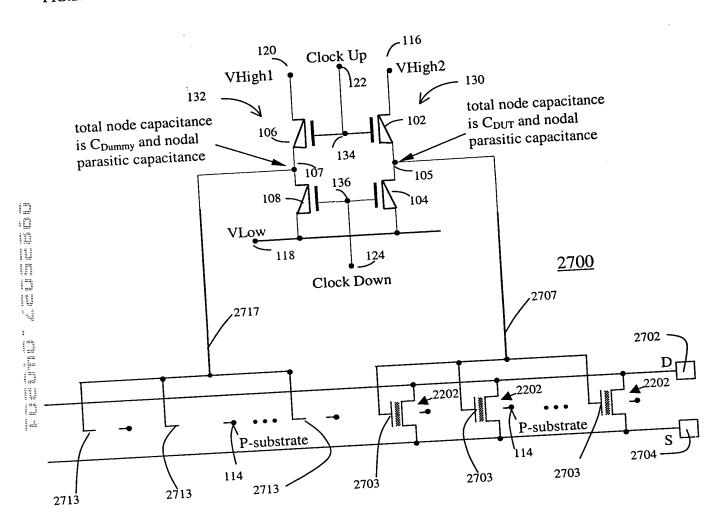
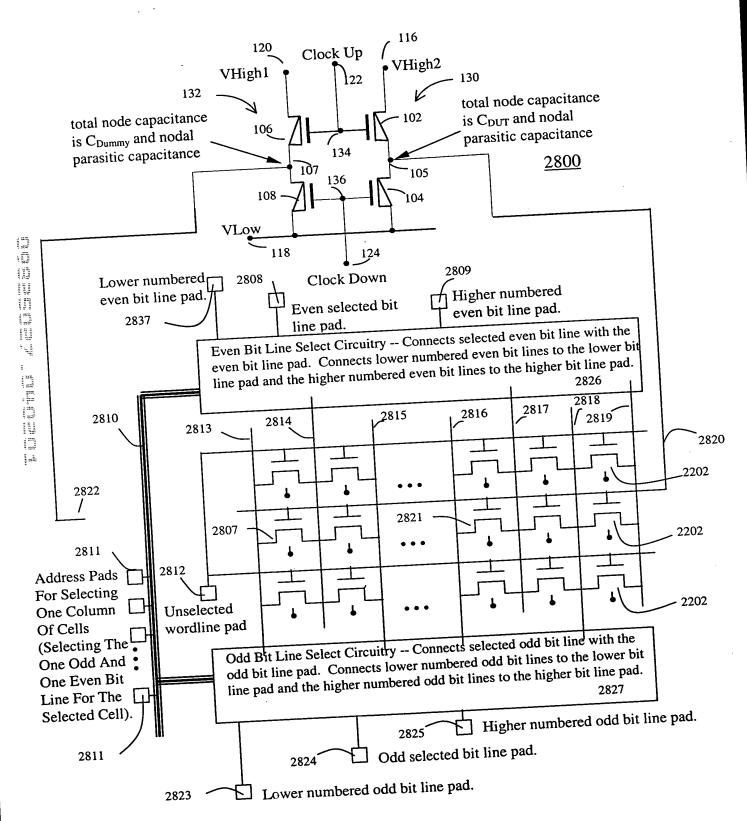
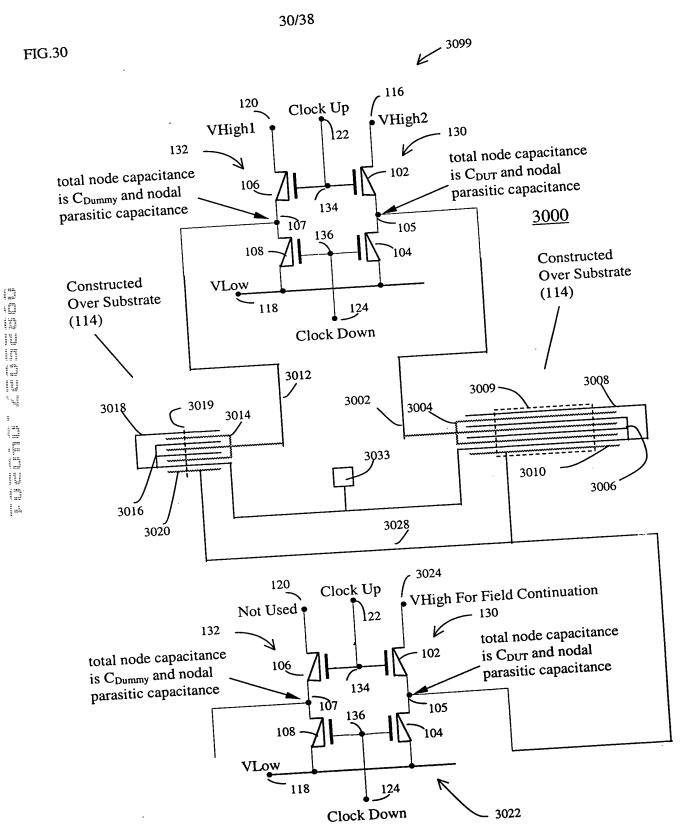


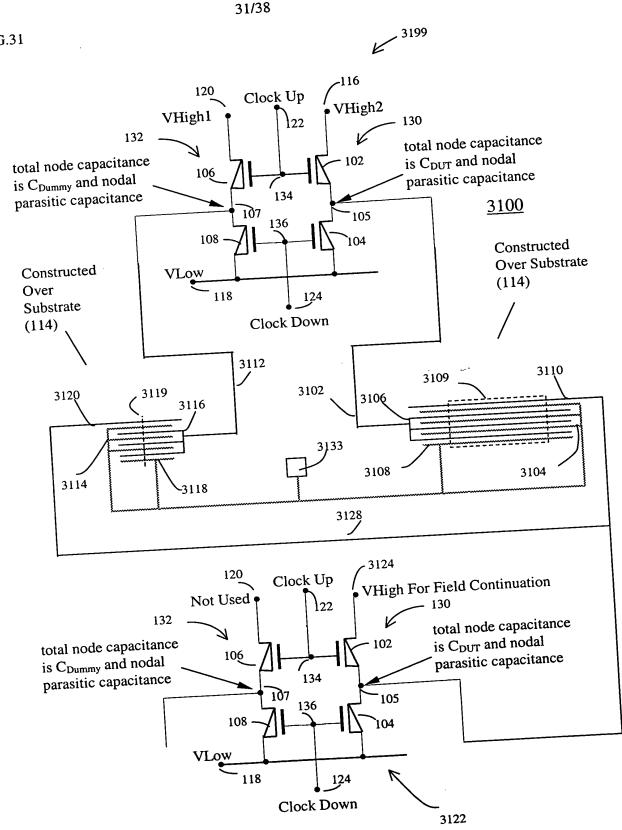
FIG.28



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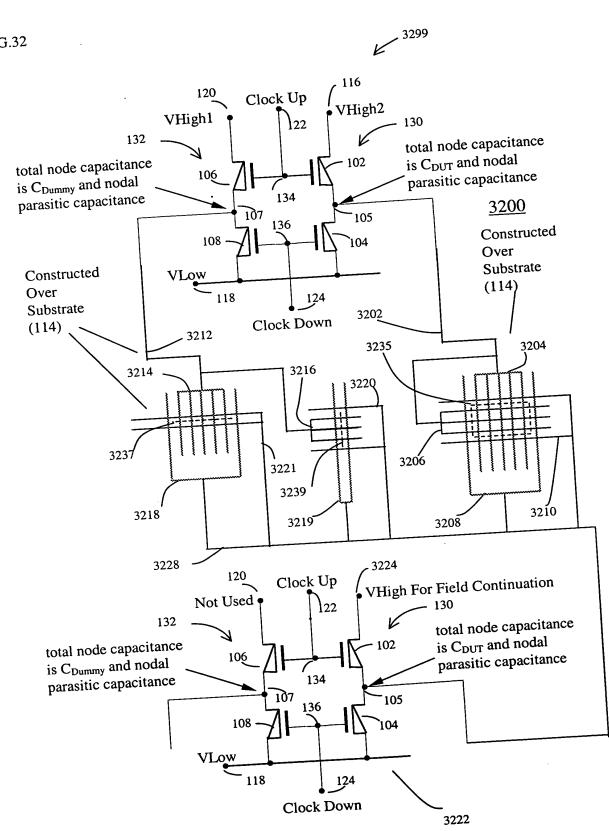
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Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

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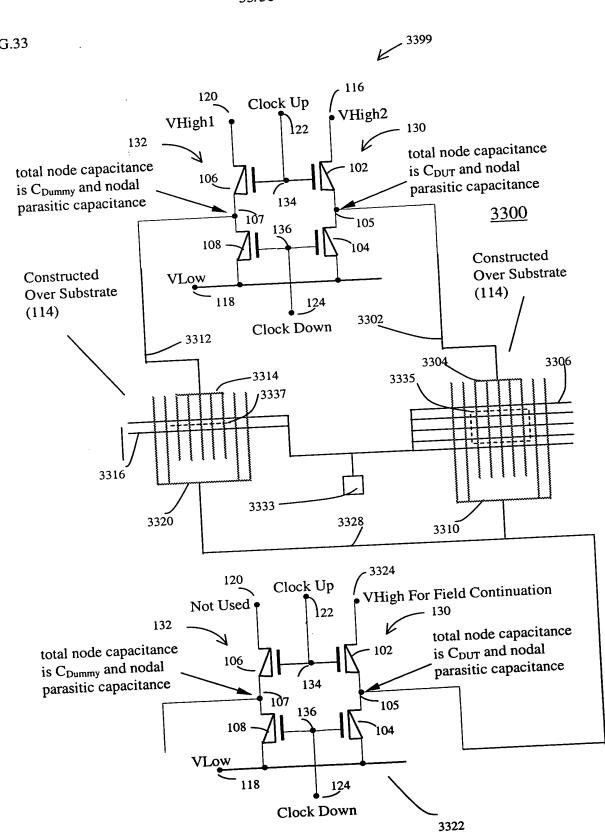


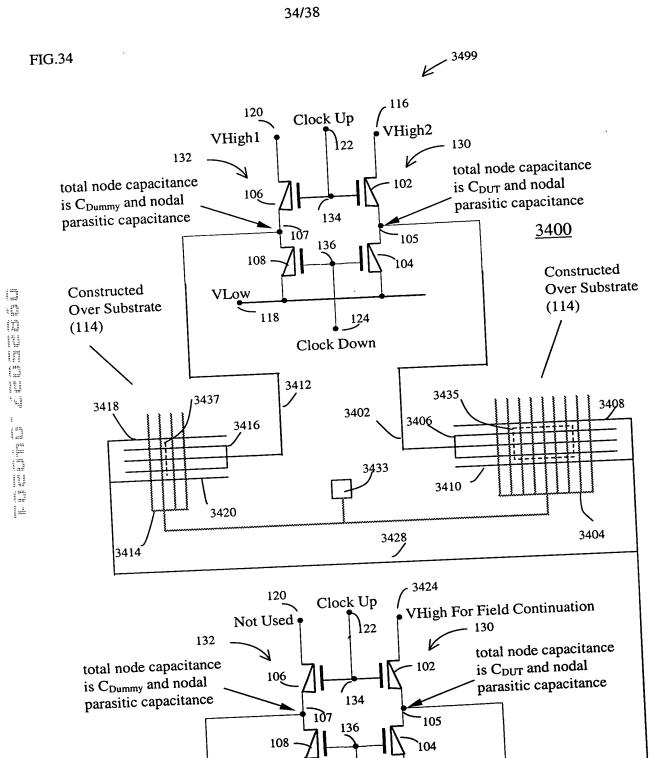
Inventor(s): David Michael Rogers, et al. Attorney Docket No.: 9076/70

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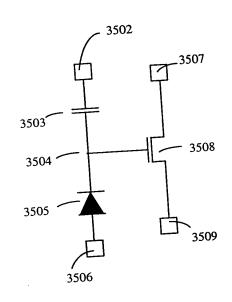
118

124

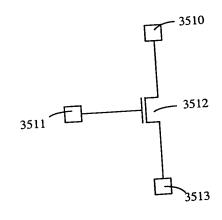
3422

Clock Down

FIG. 35



<u>3500</u>

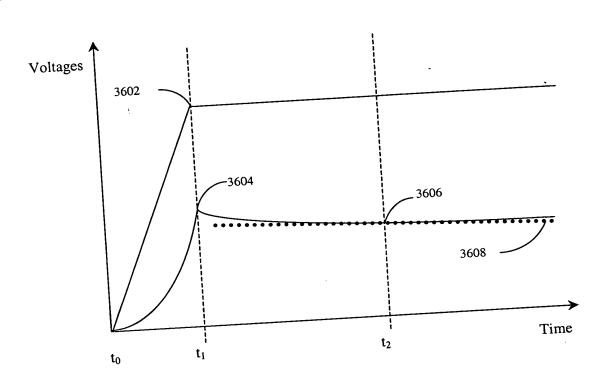


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Attorney Docket No.: 9076/70 Patent Application for: CLOG

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FIG. 36

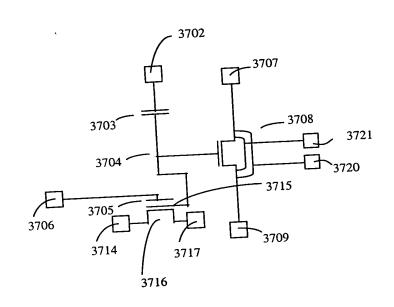
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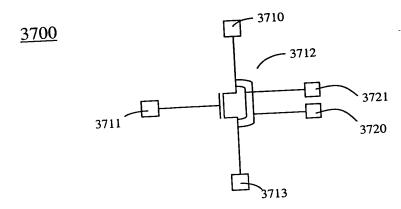


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FIG. 37

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FIG. 38

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